



Absolute Maximum Ratings (T_J = 25 °C unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak Repetitive Forward and Reverse Blocking Voltage $^{1)}$ (T _J = 25 to 125 $^{\circ}$ C, R _{GK} = 1 K Ω) MCR100-3 MCR100-4 MCR100-5 MCR100-6 MCR100-7 MCR100-8	V_{DRM} and V_{RRM}	100 200 300 400 500 600	V
Forward Current RMS (All Conduction Angles)	I _{T(RMS)}	0.8	Α
Peak Forward Surge Current, T _A = 25 °C (1/2 Cycle, Sine Wave, 60 Hz)	I _{TSM}	10	А
Circuit Fusing Considerations (t = 8.3 ms)	I ² t	0.415	A^2s
Forward Peak Gate Power (T _A = 25 °C, PW ≤ 1 µs)	P _{GM}	0.1	W
Forward Average Gate Power (T _A = 25 °C)	P _{GF(AV)}	0.01	W
Forward Peak Gate Current (T _A = 25 °C, PW ≤ 1 µs)	I _{GFM}	1	А
Reverse Peak Gate Voltage (T _A = 25 °C PW ≤ 1 µs)	V_{GRM}	5	V
Operating Junction Temperature Range at Rated V _{RRM} and V _{DRM}	Tj	- 40 to + 125	°C
Storage Temperature Range	T _{stg}	- 40 to + 150	°C

¹⁾ V_{DRM} and V_{RRM} for types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the device are exceeded.

Characteristics at T_a = 25 °C, R_{GK} = 1 K Ω unless otherwise noted.

Parameter	Symbol	Min.	Max.	Unit
Peak Forward or Reverse Blocking Current at V_{AK} = Rated V_{DRM} or V_{RRM}	I _{DRM} , I _{RRM}	-	10	μΑ
Peak Forward On-State Voltage at I _{TM} = 1 A Peak, T _A = 25 °C	V _{TM}	ı	1.7	V
Gate Trigger Current (Continuous dc) $^{1)}$ at Anode Voltage = 7 Vdc, R _L =100 Ω)	I _{GT}	-	200	μΑ
Gate Trigger Voltage (Continuous dc) at Anode Voltage = 7 Vdc, R_L = 100 Ω) at Anode Voltage = Rated V_{DRM} , R_L = 100 Ω)	V _{GT}	-	0.8	V
Holding Current at Anode Voltage = 7 Vdc, initiating current = 20 mA)	I _H	-	5	mA

¹⁾ R_{GK} current is not included in measurement.













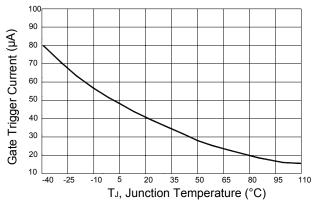


Figure 1. Typical Gate Trigger Curent Versus Junction Temperature

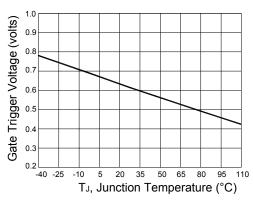
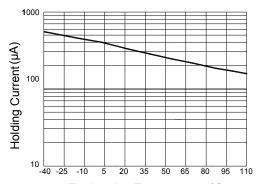


Figure 2. Typical Gate Trigger Voltage Versus Junction Temperature



TJ, Junction Temperature (°C)
Figure 3. Typical Holding Curent Versus
Junction Temperature

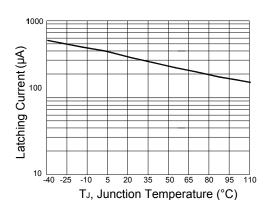


Figure 4. Typical Latching Curent Versus Junction Temperature

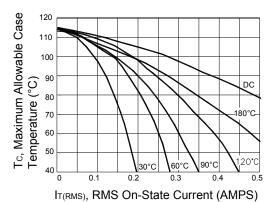


Figure 5. Typical RMS Current Derating

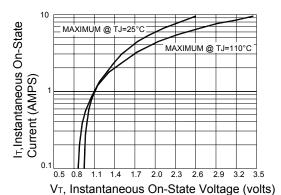


Figure 6. Typical On-State Characteristics



